

# Abstracts

## Microstrip Circuitry for Integrated Transistor Amplifiers

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*R.F. Mayo, S.P. Knight and R. Ekholdt. "Microstrip Circuitry for Integrated Transistor Amplifiers." 1967 G-MTT International Microwave Symposium Program and Digest 67.1 (1967 [MWSYM]): 132-135.*

Experimental and theoretical techniques are described which have been found useful in the development of integrated amplifiers for use at 1.2 Gc/sec and 2.0 Gc/sec. The three most important design objectives are that these amplifiers shall: (a) have adequate bandwidth. (b) be of small dimensions. (c) have low ohmic losses. These requirements often conflict and the design is usually a compromise. However, for the RCA developmental transistors which we have used, matching circuits giving 1 dB bandwidths of greater than 10% can be attained with combinations of two step transformers and open and shorted stubs.

 [Return to main document.](#)